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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/990,629	11/16/2001	Xavier Mariaud	00RO23854286	3421

7590 10/12/2004

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EXAMINER

PATEL, NITIN C

ART UNIT	PAPER NUMBER
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2116

DATE MAILED: 10/12/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/990,629

Applicant(s)

MARIAUD ET AL.

Examiner

Nitin C. Patel

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 16 November 2001.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 7-26 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 8, 10-13, 15-18, 20-22 and 24-26 is/are rejected.
- 7) ☒ Claim(s) 9, 14, 19 and 23 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 16 November 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 11/16/01.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

1. This is in responsive to the amendment filed on November 16, 2001.
2. Claims 1 – 6 have been canceled.
3. Claims 7 – 26 have added new.

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 8, 10 – 13, 15 – 18, 20 – 22, and 24 – 26 are rejected under 35 U.S.C. 103(a) as being unpatentable over Rothenbaum, US Patent 6,128,743, and further in view of Lee, US Patent 6,516,418 B1.
6. As to claims 7, 13, 18, and 22, Rothenbaum discloses an intelligent system and method for Universal Serial Bus [USB] communication and power control [connection arrangements as shown in fig. 2] comprising a detection circuitry [col. 6, lines 10 – 13, and 16 – 20] for detecting the second voltage received therefrom [detecting a signal from power supply], and logic control circuit [switching logic circuitry, col. 6, lines 10 – 13, and 16 – 20] for switching the mode of operation between the self-powered mode and bus-powered mode when one of the signal from the power supply indicates that power supply is available to the serial bus hub indicates that the power supply is available and signal from power supply to the hub indicates that power supply is not available [as represented in schematic diagram fig. 3 and fig. 4][col. 1, lines 59 – 67, col. 2,

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lines 1 – 39, col. 3, lines 11 – 67, col. 4, lines 1 – 67, col. 5, lines 1 – 11, col. 9, lines 40 – 44, and 57 – 67].

However, Rothenbaum does not teach to use memory or storage device for storing logic value corresponding to a presence [available] or absence [unavailable] of second voltage [power supply]. In summary, he does not teach the use of memory or storage device for storing the state of availability.

Lee teaches a portable computer system and method having universal serial bus ports controlled power supply with a random access memory which stores an enabled state or disabled state of the USB port, and central processing unit [CPU] determines enabled or disabled state by reading out the setting state of USB port from memory [col. 6, lines 39 – 48].

It would have been an obvious to one of ordinary skill in art, having teachings of Rothenbaum and Lee before him at the time the invention was made, to modify the USB controller logic in hub disclosed by Rothenbaum to include a use of memory to store the logic state as taught by Lee in order to obtain reduction in power consumption and preventing damage from the over-current too [col. 3, lines 50 – 53].

7. As to claims 9, 15, 20, and 24, Lee discloses a memory device to store the logic state with the use of latch [114, register] too.

8. As to claims 10, 16, and 25, Rothenbaum discloses a use of an inverter and teaches a logic for switching circuitry for mode switching it can be implemented by one of an ordinary skill in art using different logic elements with NOR, NAND, INV to generate same output.

9. As to claims 11, 17, 21, and 26, Lee discloses the first peripheral device a micro controller [central processing unit [CPU], fig. 2] with circuit for interrupt [it is inherent to CPU]

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and a memory to store the logic state with register [114, fig. 4] therefore he teaches to use different logic elements and arrangements too.

10. As to claim 12, Rothenbaum teaches USB comprises of the first conductor wire in USB a data conductor wire [col. 3, lines 13 – 16].

Allowable Subject Matter

11. Claims 8, 14, 19, and 23 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nitin C. Patel whose telephone number is 703-305-3994. The examiner can normally be reached on 8:00am - 4:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne H. Browne can be reached on 703-308-1159. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Nitin C. Patel
October 7, 2004


LYNNE H. BROWNE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 3600-2100